

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.: 09/683,677

Applicant(s): Devins et al.

Filed.: February 1, 2002

Art Unit: 2128

Dkt. No.: BUR920010098US1

Examiner: ALHIJA, SAIF A.

Title: **METHOD OF SWITCHING EXTERNAL MODELS IN AN AUTOMATED
SYSTEM-ON-CHIP INTEGRATED CIRCUIT DESIGN VERIFICATION SYSTEM**

Honorable Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Applicants request the attached amendment be entered prior to Examination of the
Application on the merits.